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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/086,786	02/28/2002	Russell B. Stuber	L13.12-0174/01-262	8978
75	590 06/29/2006		EXAMINER	
Sandeep Jaggi			KING, JUSTIN	
LSI LOGIC CORPORATION M/S D-106			ART UNIT	PAPER NUMBER
1551 McCarthy Boulevard			2111	
Milpitas, CA 95035			DATE MAILED: 06/29/2006	

Please find below and/or attached an Office communication concerning this application or proceeding.

## Advisory Action Before the Filing of an Appeal Brief

Application No.	Applicant(s)		
10/086,786	STUBER ET AL.		
Examiner	Art Unit		
Justin I. King	2111		

	Justin I. King	2111	
The MAILING DATE of this communication appe	ars on the cover sheet with the	orrespondence add	ress
THE REPLY FILED 05 June 2006 FAILS TO PLACE THIS APP			
<ol> <li>The reply was filed after a final rejection, but prior to or on this application, applicant must timely file one of the follow places the application in condition for allowance; (2) a No a Request for Continued Examination (RCE) in compliance time periods:</li> </ol>	the same day as filing a Notice of ving replies: (1) an amendment, aftice of Appeal (with appeal fee) in	Appeal. To avoid aba fidavit, or other evider compliance with 37 C	nce, which FR 41.31; or (3)
a) The period for reply expires 3 months from the mailing date b) The period for reply expires on: (1) the mailing date of this A no event, however, will the statutory period for reply expire to Examiner Note: If box 1 is checked, check either box (a) or ( TWO MONTHS OF THE FINAL REJECTION. See MPEP 7	dvisory Action, or (2) the date set forth ater than SIX MONTHS from the mailin (b). ONLY CHECK BOX (b) WHEN THI 06.07(f).	g date of the final rejecti E FIRST REPLY WAS F	on. ILED WITHIN
Extensions of time may be obtained under 37 CFR 1.136(a). The date have been filed is the date for purposes of determining the period of ex under 37 CFR 1.17(a) is calculated from: (1) the expiration date of the set forth in (b) above, if checked. Any reply received by the Office later may reduce any earned patent term adjustment. See 37 CFR 1.704(b) NOTICE OF APPEAL	tension and the corresponding amount shortened statutory period for reply orig than three months after the mailing da	of the fee. The appropri	iate extension fee ce action; or (2) a
<ol> <li>The Notice of Appeal was filed on A brief in comp filing the Notice of Appeal (37 CFR 41.37(a)), or any exte a Notice of Appeal has been filed, any reply must be filed AMENDMENTS</li> </ol>	nsion thereof (37 CFR 41.37(e)), to	avoid dismissal of th	ns of the date of ne appeal. Since
3. The proposed amendment(s) filed after a final rejection,  (a) They raise new issues that would require further co  (b) They raise the issue of new matter (see NOTE belo  (c) They are not deemed to place the application in beto	nsideration and/or search (see NO w);	TE below);	
appeal; and/or  (d) They present additional claims without canceling a  NOTE: See Continuation Sheet. (See 37 CFR 1.1	corresponding number of finally re		110 100 000 101
<ul> <li>4.  The amendments are not in compliance with 37 CFR 1.1.</li> <li>5.  Applicant's reply has overcome the following rejection(s)</li> <li>6.  Newly proposed or amended claim(s) would be all non-allowable claim(s).</li> </ul>	21. See attached Notice of Non-Co:		
7.  For purposes of appeal, the proposed amendment(s): a) how the new or amended claims would be rejected is pro The status of the claim(s) is (or will be) as follows: Claim(s) allowed: None. Claim(s) objected to: 5 and 8. Claim(s) rejected: 1-4,6 and 7. Claim(s) withdrawn from consideration: 9-16.	⊠ will not be entered, or b) □ wvided below or appended.	II be entered and an e	explanation of
<ul> <li>AFFIDAVIT OR OTHER EVIDENCE</li> <li>The affidavit or other evidence filed after a final action, be because applicant failed to provide a showing of good an was not earlier presented. See 37 CFR 1.116(e).</li> </ul>	nt before or on the date of filing a N d sufficient reasons why the affida	lotice of Appeal will <u>no</u> vit or other evidence is	ot be entered s necessary and
<ol> <li>The affidavit or other evidence filed after the date of filing entered because the affidavit or other evidence failed to a showing a good and sufficient reasons why it is necessar</li> <li>The affidavit or other evidence is entered. An explanation</li> </ol>	overcome <u>all</u> rejections under appe y and was not earlier presented. S	eal and/or appellant fa See 37 CFR 41.33(d)(	ils to provide a 1).
REQUEST FOR RECONSIDERATION/OTHER  11.   The request for reconsideration has been considered by			
See Continuation Sheet.			
<ul><li>12. ☐ Note the attached Information Disclosure Statement(s).</li><li>13. ☐ Other:</li></ul>		MARK H. RINEI JPERVISORY PATENT	

**TECHNOLOGY CENTER 2100** 

## Continuation Sheet (PTO-303)

Continuation of 3. and 11. NOTE:

Applicant has amended claims to include new limitation of "next hightest in the return command register", which has not been serached and considered on the record.

Applicant argues that the prior arts on record do not disclose staging register: Marisetty disclose a prediction technique to further enhance the conventional split transaction. Marisetty discloses an early bus request logic and bus grant prediction/arbitration logic to further reduce bus latency along with the conventional FIFO buffer. Marisetty's early bus prediction mechanism is equivalent to the claimed staging register and control device, which stores the identification of the master device and to release the split of the master device.

Applicant argues that the prior arts on record do not disclose the control device as claimed: As stated in the previous Office Action and acknowledged by the Applicant, the means to release the split master is equivalent to the claimed control device. The claims are rejected based on the combination of the admitted prior art and Marisetty. Marisetty discloses prioritizing based on first-in-first-out order (FIFO buffer). Marisetty teaches one the early prediction technique to further enhance the bus performance. The claimed split of the next master device will be the next command in the FIFO. Applicant argues that Marisetty does not disclose the control device. Marisetty discloses that the split transaction is well known in the computer art. The inherent means to manage the split transaction is equivalent to the control device. Marisetty also discloses an early bus request logic and bus grant prediction/arbitration logic to further reduce bus latency along with the conventional FIFO buffer. Marisetty's early bus prediction mechanism is equivalent to the claimed staging register and control device.

Applicant argues that neither reference discloses a control that releases the master device that is next highest in a return command register: Examiner believes that Applicant may have meant release the split of the master device